

SMART Memory Test Lab Report



| MOTHERBOARD | MANUFACTURER | CHIPSET | TEST NUMBER | TEST STATUS |
|-------------|--------------|------------|-------------|-------------|
| S5520SCR | INTEL | INTEL 5520 | SMTL0057 | PASS |

Module Information

| | | | |
|-----------------------|---------------------|-------------------|--------------------------|
| Manufacturer Name: | SMART MODULAR TECH | Module Type: | DDR3 REGISTERED ECC DIMM |
| Manufacturer P/N: | SG2567RD312893-SF | CAS Latency: | C999 |
| Memory Capacity: | 2GB | Assembly Type: | SINGLE BOARD NON STACKED |
| Module Configuration: | 6 PER CPU /12 TOTAL | Low Profile: | NO |
| DRAM Supplier: | SAMSUNG | Register P/N: | INPHI INSSTE32882LV-GS02 |
| DRAM Part Number: | K4B1G0846F-HCH9 | PLL P/N: | N/A |
| PCB Part Number: | M393B5673EH1-V072 | Dual Die Package: | MONOLITHIC |
| PCB Layer Count: | 6 | Comment: | |

MINIMUM SYSTEM INFORMATION

| | |
|-----------------|--------------------------|
| System Name: | S5520SCR |
| Serial Number: | QSHV00800136 |
| BIOS Version: | AMI S5500.86B.01.00.0045 |
| Processor Code: | INTEL XEON X5650 |
| OS Memory Size: | 12GB |
| OS Version: | LINUX SMP 2.6.28.10 |
| Test Version: | HPL XLINPAK 10.2.3 MKL |
| Comment: | |

MAXIMUM SYSTEM INFORMATION

| | |
|-----------------|--------------------------|
| System Name: | S5520SCR |
| Serial Number: | QSHV00800136 |
| BIOS Version: | AMI S5500.86B.01.00.0045 |
| Processor Code: | INTEL XEON X5650 |
| OS Memory Size: | 24GB |
| OS Version: | LINUX SMP 2.6.28.10 |
| Test Version: | HPL XLINPAK 10.2.3.MKL |
| Comment: | |

PRE-TESTING INFORMATION

| | |
|------------------|-------|
| Board Revision: | |
| Insertion Test: | PASS |
| SPD Check: | PASS |
| DDR Speed Check: | PASS |
| Boot Time: | 1 MIN |

Voltage Margining Info

| | |
|-----------|-----------|
| Volt % | +/-3% |
| Volt Low | 1.45 volt |
| Volt High | 1.55 volt |

TESTING DETAILS FOR VOLT LOW

| | | | |
|------------------|-----|-------|------------|
| Temperature: | 55C | Time: | 12HRS |
| Start Date: | | | 11/1/2010 |
| Stop Date: | | | 11/19/2010 |
| HPL Test Status: | | | PASS |
| DDR Speed: | | | 1333MHz |
| Summary Min: | | | P |
| Summary Max: | | | P |

Test Status: PASS

TESTING DETAILS FOR VOLT HIGH

| | | | |
|------------------|-----|-------|------------|
| Temperature: | 55C | Time: | 12HRS |
| Start Date: | | | 11/2/2010 |
| Stop Date: | | | 11/22/2010 |
| HPL Test Status: | | | PASS |
| DDR Speed: | | | 1333MHz |
| Summary Min: | | | P |
| Summary Max: | | | P |

Test Status: PASS

Serial Number List

| | | | |
|----------------------|----------------------|----------------------|----------------------|
| 1) SFR2010102892906 | 2) SFR2010102892905 | 3) SFR2010102892907 | 4) SFR2010102892904 |
| 5) SFR2010102892908 | 6) SFR2010102892902 | 7) SFR2010102892899 | 8) SFR2010102892903 |
| 9) SFR2010102892905 | 10) SFR2010102892909 | 11) SFR2010102892911 | 12) SFR2010102892910 |
| 13) SFR2010102892898 | 14) SFR2010102892901 | 15) | 16) |

CONTACT INFORMATION

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Test Traveler Notes:

VERIFICATION

Verified by: Jim Schwarz

Test Technician: Mark Durand

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